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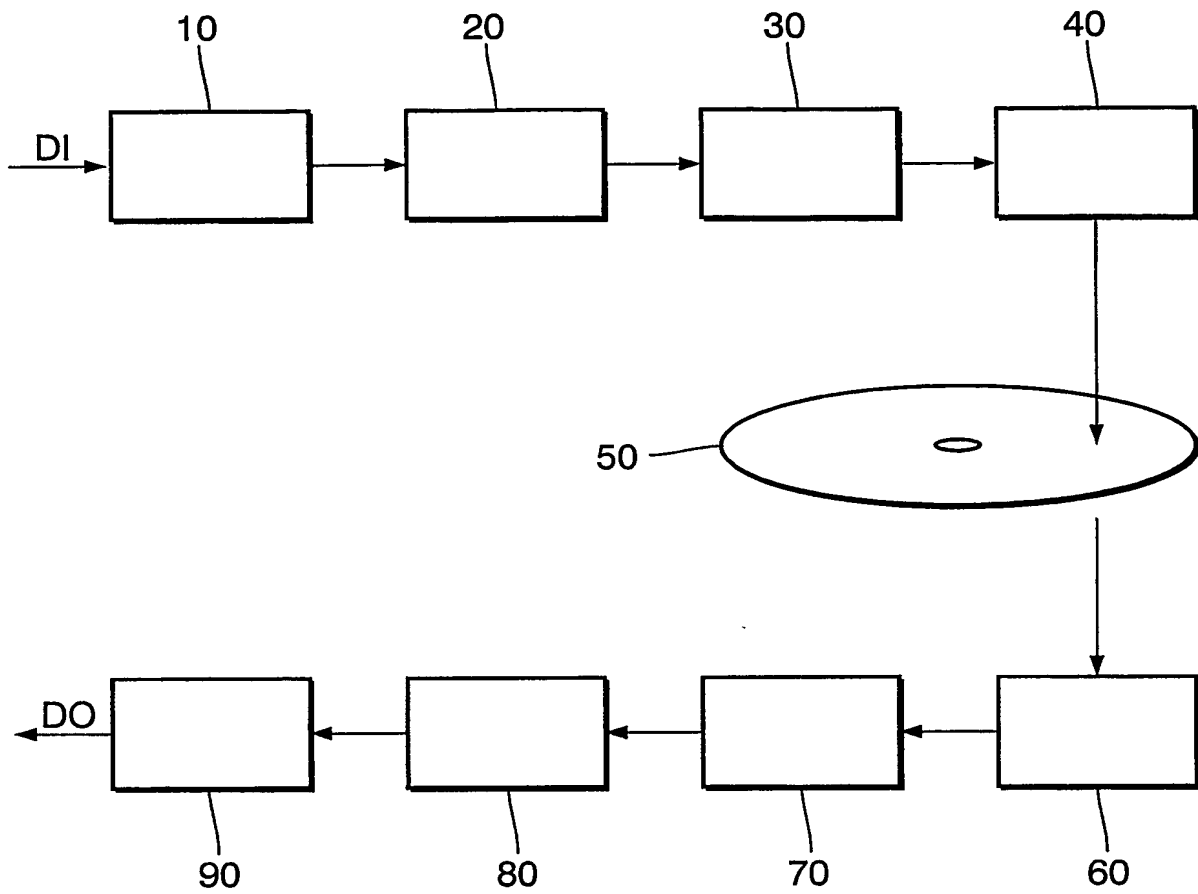


FIG.1

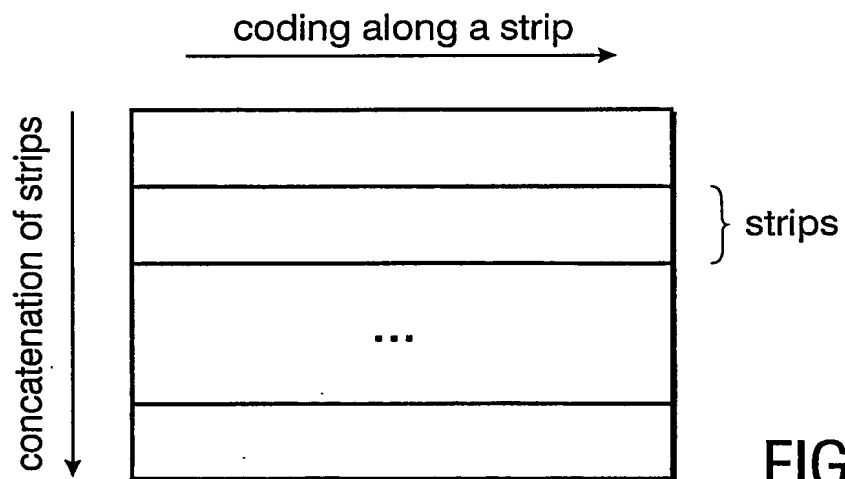


FIG.2

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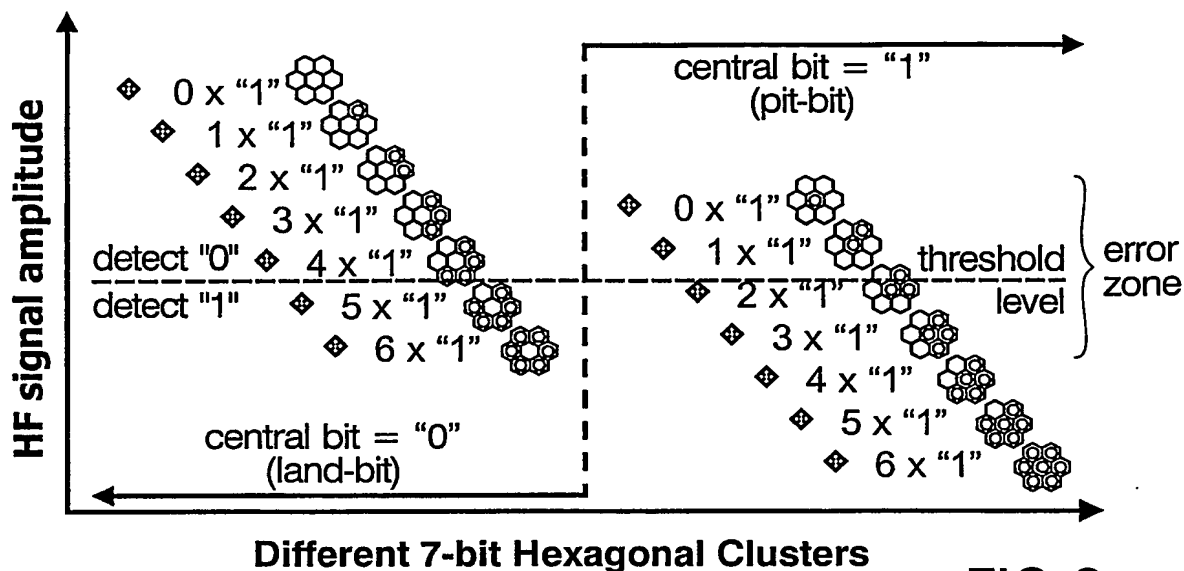


FIG.3

2D Channel
Impulse Response

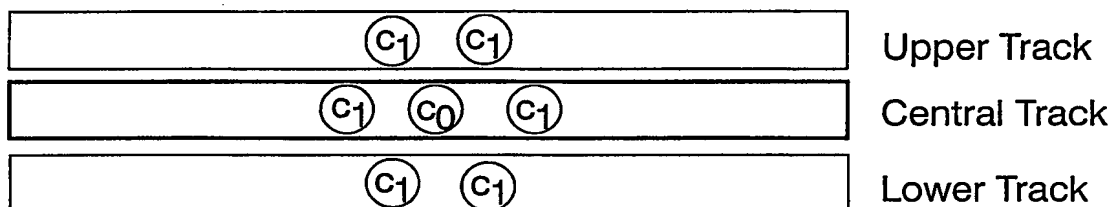


FIG.4

4-state FSM

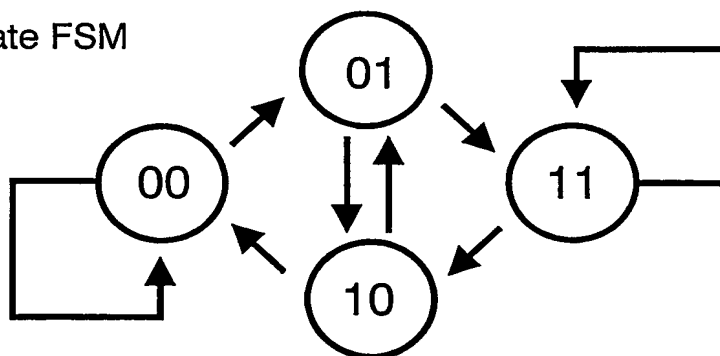
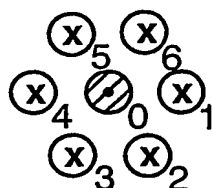


FIG.5

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⊗ = nearest-neighbour bit

⊘ = central-bit or core-bit

FIG.6

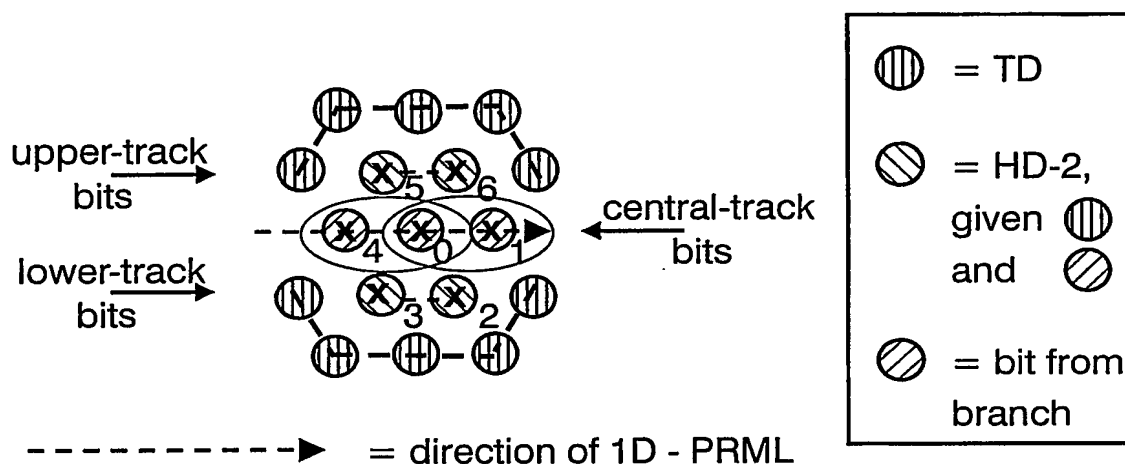


FIG.7

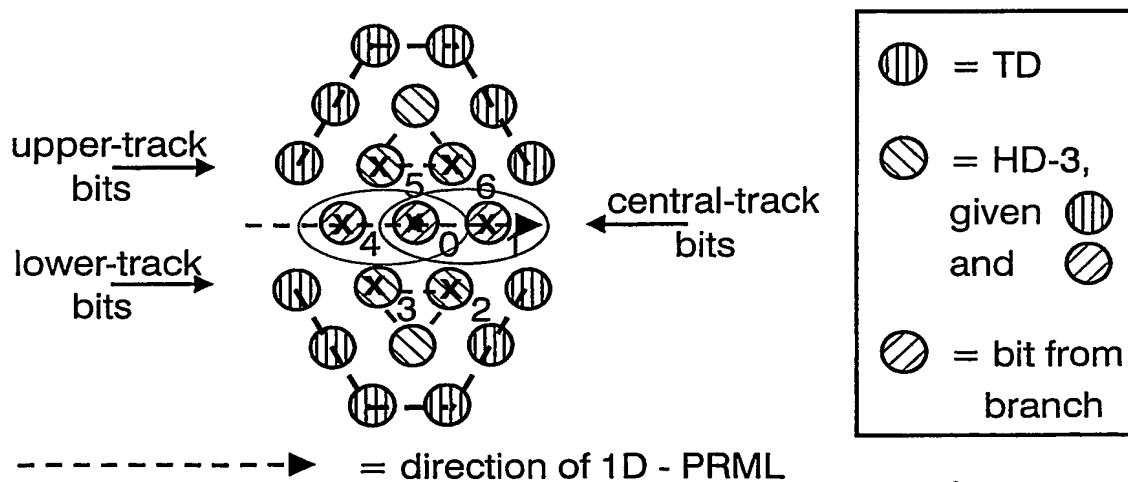


FIG.8

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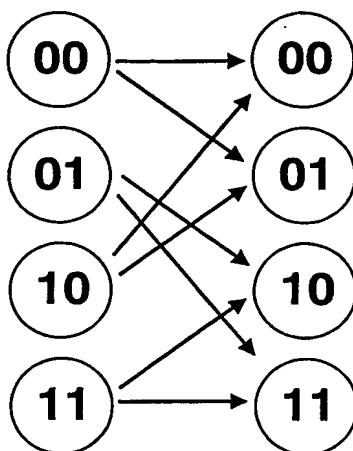


FIG.9

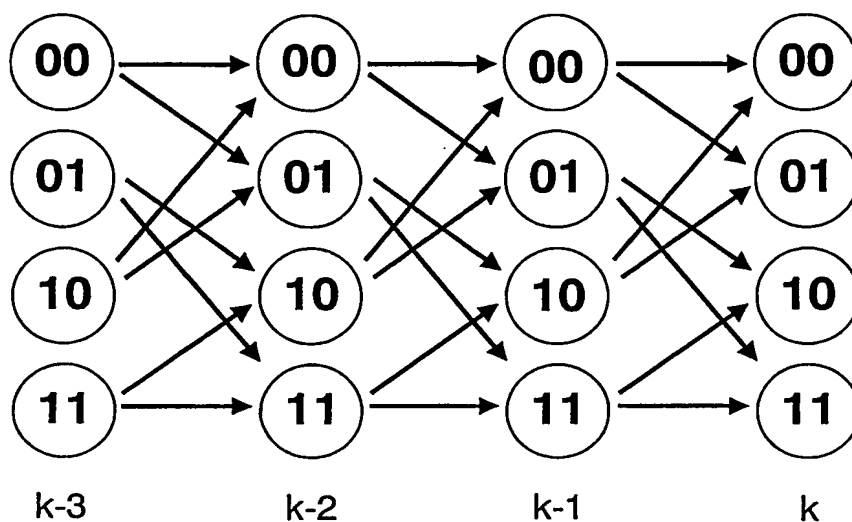


FIG.10

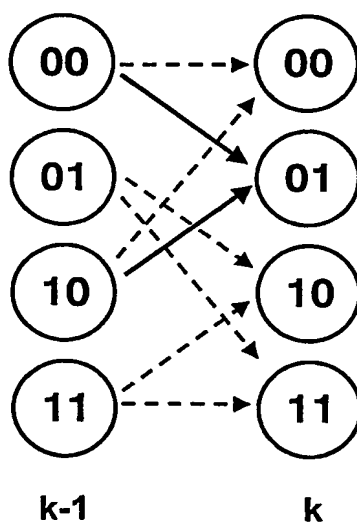
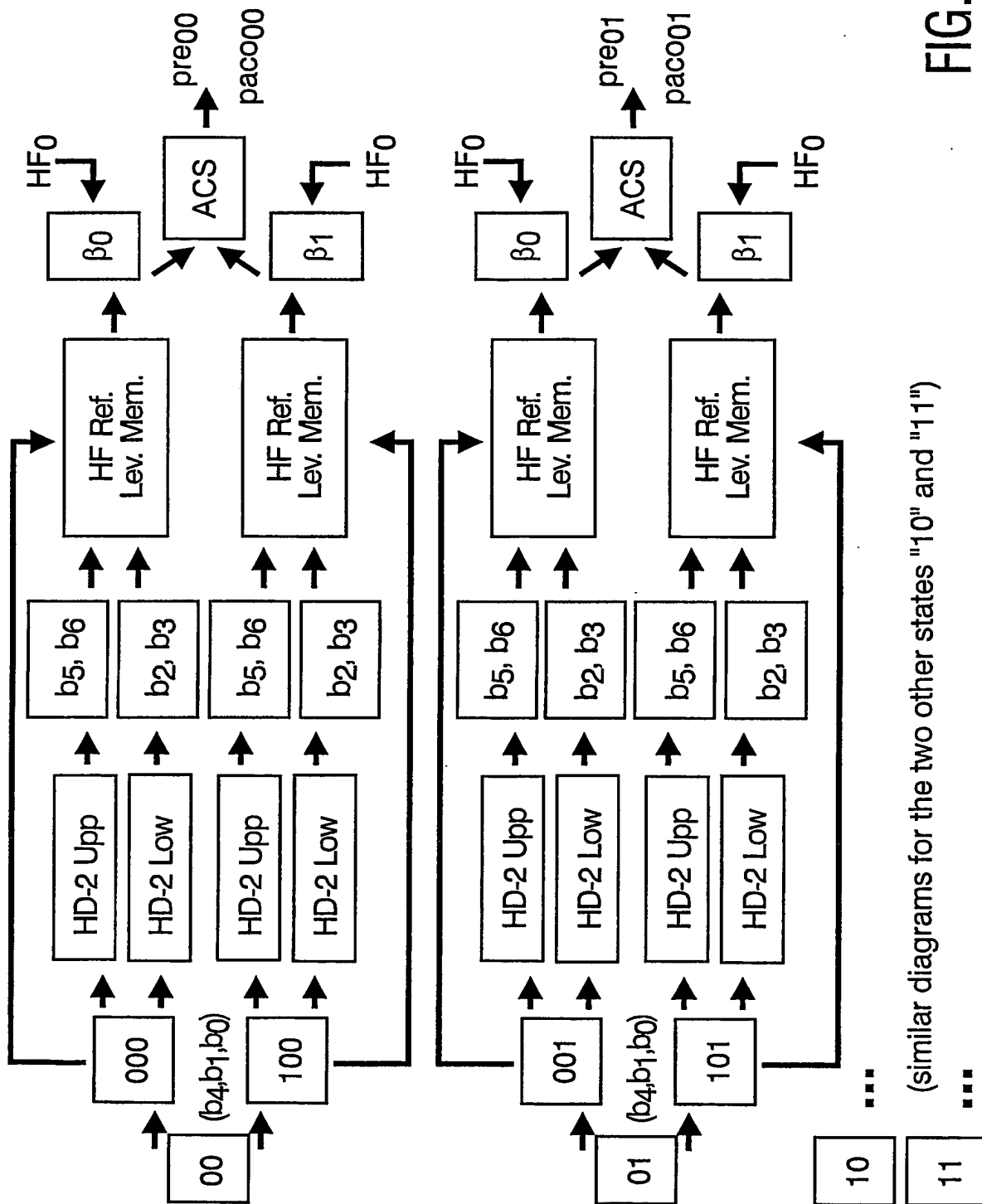
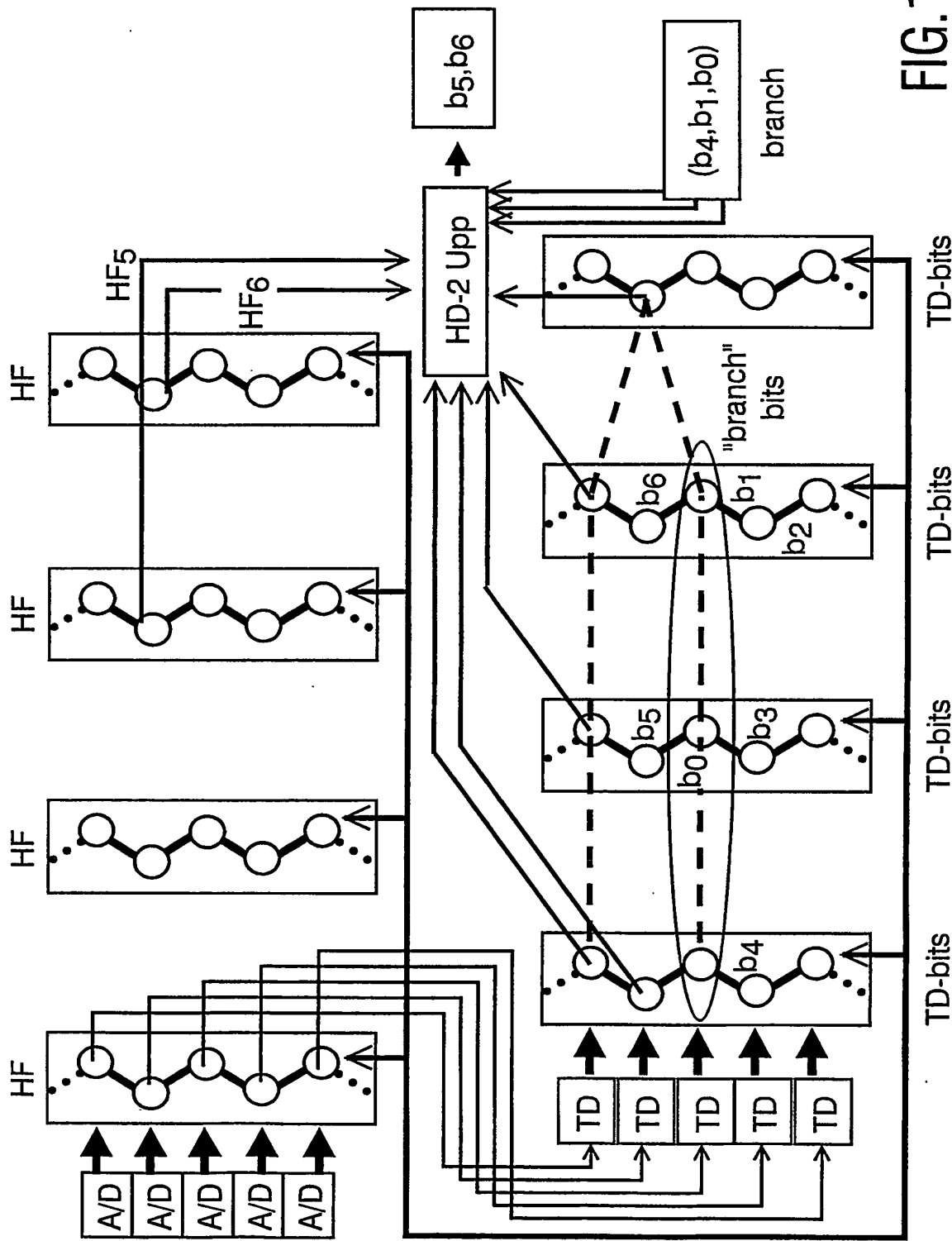


FIG.11



(similar diagrams for the two other states "10" and "11")



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HF Reference - Levels
for HD-2 Bit-Unit

bits HD-2 bit-unit b_0 b_1	Reference-Level for bit b_0	Reference-Level for bit b_1
0 0	$R_{\underline{0}0}$	$R_{0\underline{0}}$
0 1	$R_{\underline{0}1}$	$R_{0\underline{1}}$
1 0	$R_{\underline{1}0}$	$R_{1\underline{0}}$
1 1	$R_{\underline{1}1}$	$R_{1\underline{1}}$

FIG.14

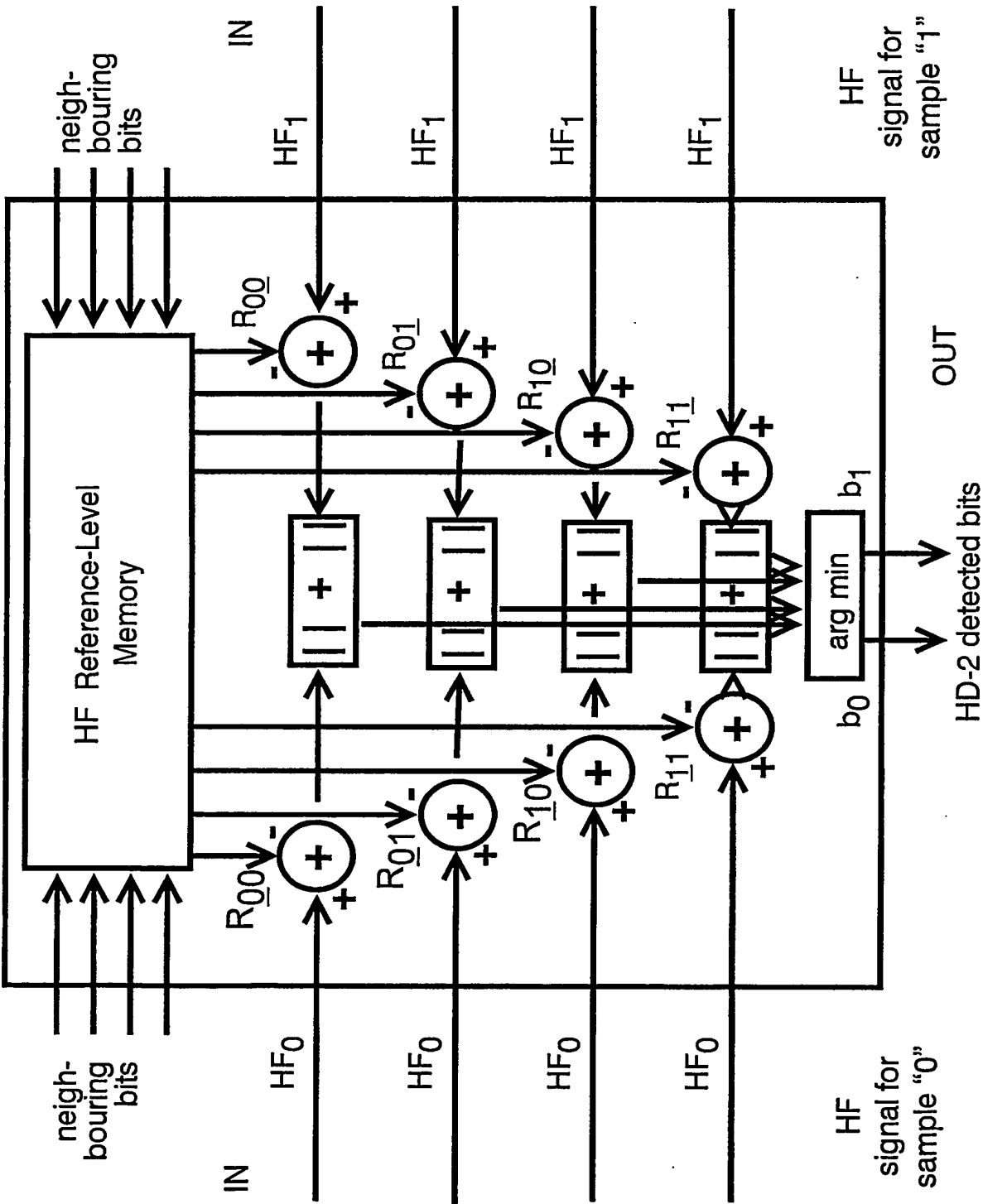


FIG.15

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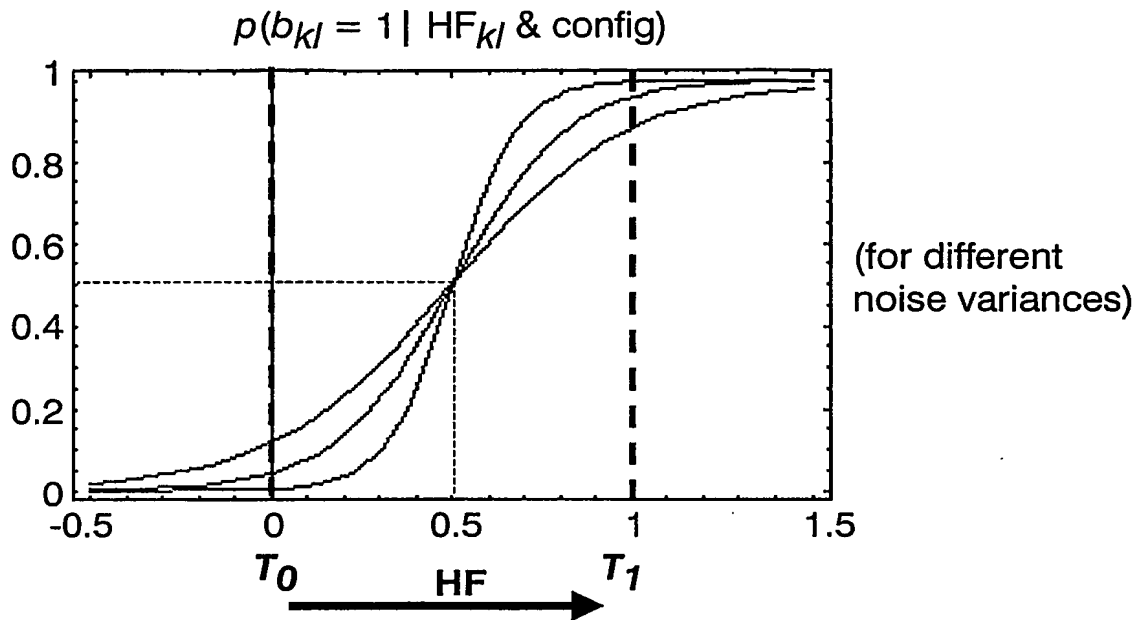


FIG.16

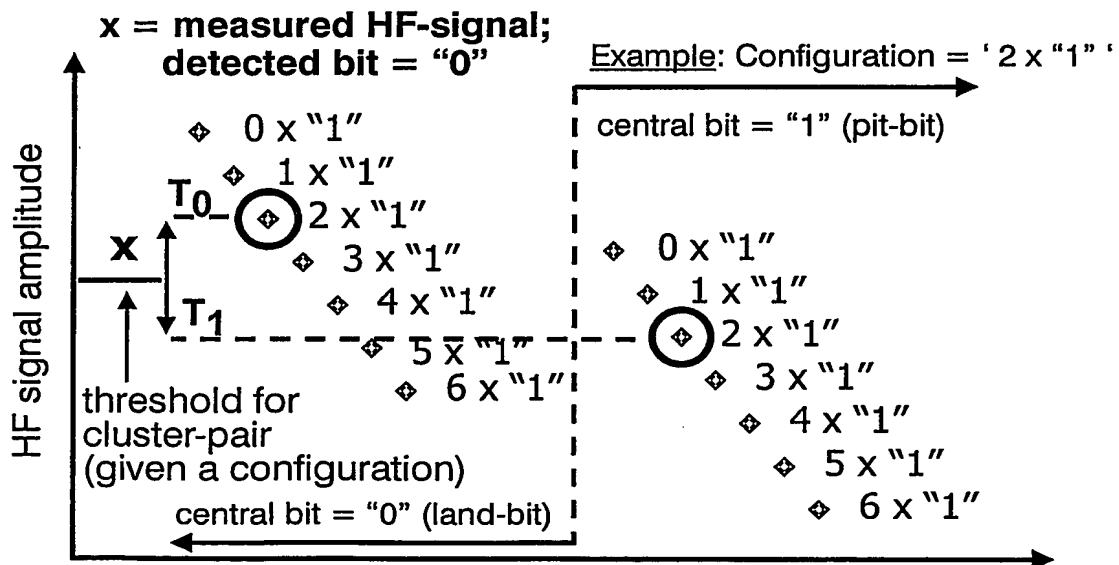


FIG.17

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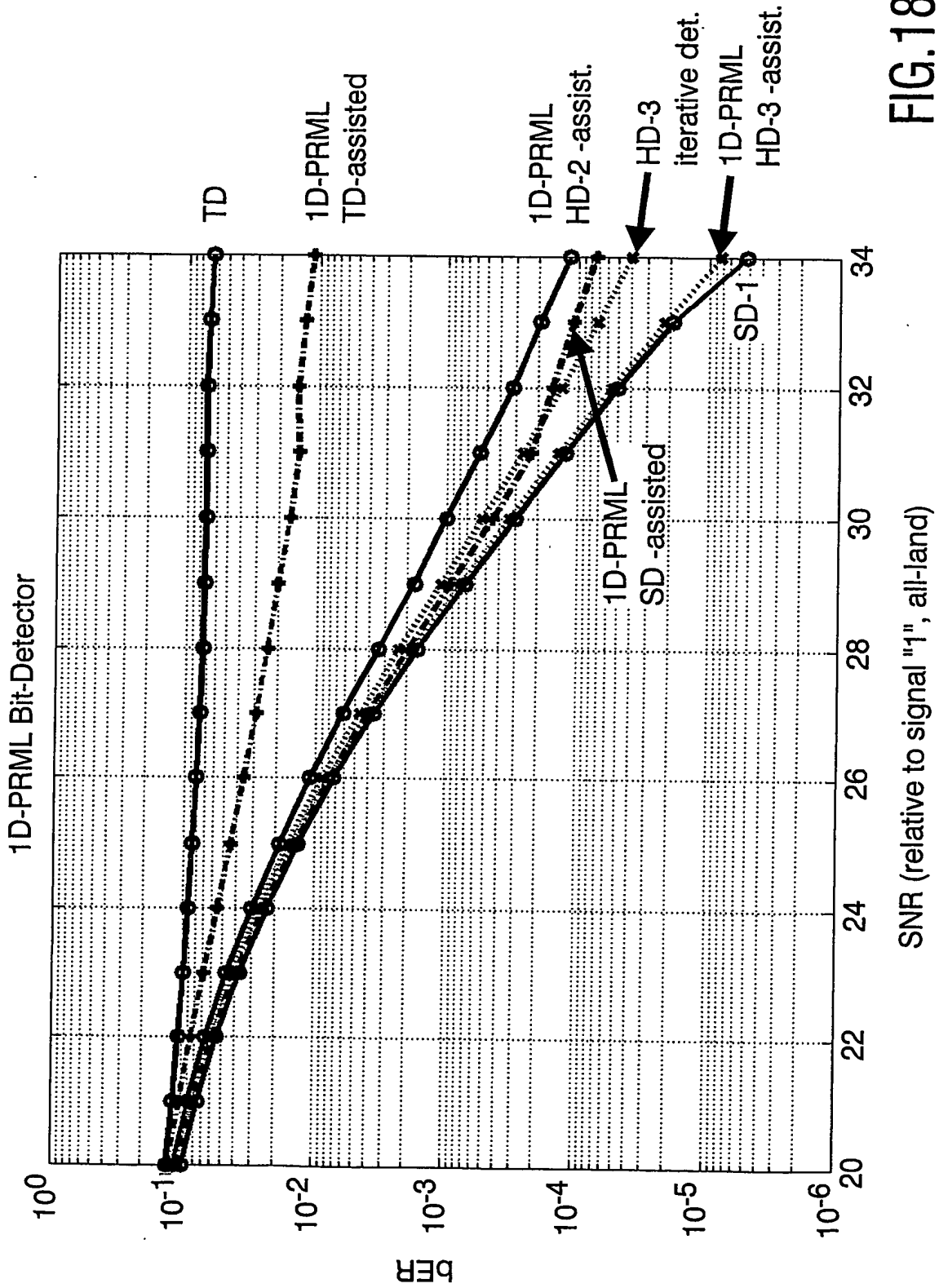


FIG.18